

WHAT IS CLAIMED IS:

1. A display device comprising:

a display area;

5 a plurality of scanning lines arranged in a first direction;

a plurality of signal lines arranged in a second direction;

10 a signal line driving circuit successively selecting a prescribed signal line from said plurality of signal lines and supplying a video signal; and

a scanning line driving circuit successively selecting a prescribed scanning line from said plurality of scanning lines and supplying a scanning signal, wherein

15 said signal line driving circuit and said scanning line driving circuit are arranged on the same peripheral side of said display area in a cascaded manner.

2. The display device according to claim 1, wherein

20 said scanning line driving circuit is arranged outward beyond said signal line driving circuit.

3. The display device according to claim 2, wherein

25 said signal line driving circuit includes a plurality of shift registers, a plurality of buffers and a plurality

of analog switches, and

said shift registers, said buffers and said analog switches for adjacent said signal lines are arranged in a cascaded manner respectively.

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4. The display device according to claim 3, further comprising a video signal line connected to said analog switches, wherein

said video signal line includes:

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a first video signal line connected to said analog switches of odd stages, and

a second video signal line connected to said analog switches of even stages.

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5. The display device according to claim 3, wherein

a wire from said scanning line driving circuit is input in said display area through said shift registers,

said buffers and said analog switches arranged in a

cascaded manner and shift registers, buffers and analog

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switches arranged adjacently thereto in a cascaded manner.

6. The display device according to claim 5, wherein

said wire from said scanning line driving circuit is connected to said scanning lines arranged in the row

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direction in said display area column-directionally

through said shift registers, said buffers and said analog switches arranged in a cascaded manner and said shift registers, said buffers and said analog switches arranged adjacently thereto in a cascaded manner.

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7. The display device according to claim 1, wherein said display area, said signal line driving circuit and said scanning line driving circuit are formed on a display panel.

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8. The display device according to claim 1, further comprising a plurality of display panels each including said display area, said scanning lines, said signal lines, said signal line driving circuit and said scanning line driving circuit, wherein

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said signal line driving circuit and said scanning line driving circuit in each of said plurality of display panels are arranged on the same peripheral side of said display area in a cascaded manner, and

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said plurality of display panels are connected with each other at least on a side of each said display panel other than the side provided with said signal line driving circuit and said scanning line driving circuit.

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9. The display device according to claim 8, wherein

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5        said plurality of display panels are connected with each other at least on two sides or three sides of each said display panel other than the side provided with said signal line driving circuit and said scanning line driving circuit.

10        10. The display device according to claim 8, wherein said display panels are connected with each other in an even number of at least six.

15        11. The display device according to claim 1, wherein said display area includes a plurality of pixels arranged in the form of a matrix.

20        12. The display device according to claim 1, including a liquid crystal display.

25        13. The display device according to claim 1, including an EL (electroluminescence) display.

      14. The display device according to claim 13, wherein a current supply line is arranged on said display area of said EL display.

      15. The display device according to claim 13, wherein

said display area of said EL display includes a plurality of pixels arranged in the form of a matrix, and each said pixel includes a switching transistor, a capacitor, an EL element and a driving transistor.

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